

Abstract of the Disclosure

The liquid crystal display of the present invention comprising: a gate electrode line formed on an insulating substrate; a source electrode line including a source electrode intersected with said gate electrode line via an insulating film, a thin film transistor located in a vicinity of a portion in which said gate electrode line is intersected with said source electrode line; two drain electrode lines, each including two drain electrodes in said thin film transistor, said drain electrode line being connected with a pixel electrode; wherein said thin film transistor includes said two drain electrode lines located on both sides of said source electrode; said two drain electrodes being formed at a place where each end portion of said two drain electrode lines opposed to said source electrode is superposed with said gate electrode line.